

100%
Document 10
ISSUE Date
Pages
Title
Current OK
Current X

Search
List

6033924

133340) (emitt\$3 emiss\$3).ti.
(11787) ((emitt\$3 emiss\$3).ti.) and cathode
(11787) (emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)
(4501) (((emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)) and ((emitt\$3 emiss\$3) n...
(493) (((emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)) and ((emitt\$3 emiss\$3) n...
(350) (((emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)) and ((emitt\$3 emiss\$3)...
(305) (((emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)) and ((emitt\$3 emiss\$3)...
(0) (((emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)) and ((emitt\$3 emiss\$3)...
(54) (((emitt\$3 emiss\$3) and (((emitt\$3 emiss\$3).ti.) and cathode)) and ((emitt\$3 emiss\$3)...
(18118) emitter with (method process)
(3373) (emitter with (method process)) and cathode
(168) ((emitter with (method process)) and cathode) and tunneling
(163) (((emitter with (method process)) and cathode) and tunneling) and electron
(60) (((emitter with (method process)) and cathode) and tunneling) and electron) and protect\$3
(48) (((emitter with (method process)) and cathode) and tunneling) and electron) and protec...
(1) "5656525".PN.
(1) "5616368".PN.
(1) "5588894".PN.
(1) "5578900".PN.
(1) "5223766".PN.
(1) "5083958".PN.
(1) "5063323".PN.

FIG. 7

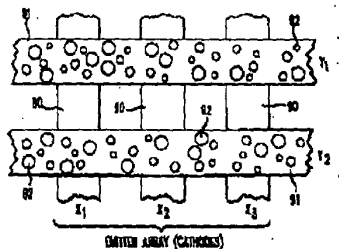
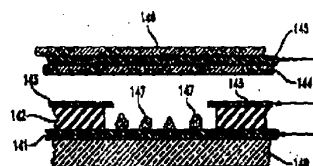


FIG. 8



(25) In typical applications the gate electrodes and emitters are deposited in skewed perpendicular stripes to define a grid of emitting regions. FIG. 7 illustrates columns 90 of an emitter array and rows 91 of an apertured gate conductor array forming an x-y matrix of emitter regions. Emission is through apertures 92. These rows and columns can be prepared by low-cost screen printing of emitter material (e.g. in stripes of 100 μm width) and physical vapor deposition of the gate conductor through a strip metal mask with, for example, 100 μm wide parallel gaps. Depending on the activation voltage of a particular column of gate and a particular row of emitter, a specific pixel can be selectively activated at the intersection of column and row to emit electrons.

(26) The preferred use of these low voltage emitters is in the fabrication of field emission devices such as electron emission flat panel displays. FIG. 8 is a schematic cross section of an exemplary flat panel display using low voltage particulate emitters. The display comprises a cathode 141 including a plurality of low voltage particulate emitters 147 and an anode 145 disposed in spaced relation from the emitters within a vacuum seal. The anode conductor 145 formed on a transparent insulating substrate 146 is provided with a phosphor layer 144 and mounted on support pillars (not shown). Between the cathode and the anode and closely spaced from the emitters is a perforated conductive gate layer 143. Conveniently the gate 143 is spaced from the cathode 141 by a thin insulating layer 142.

CAST Advanced Find

Find what: 141

Find Next

Find

Direction

Match case

Match

Match

Match

Find

ion devices employin
among particle emitt